

FIG. 1

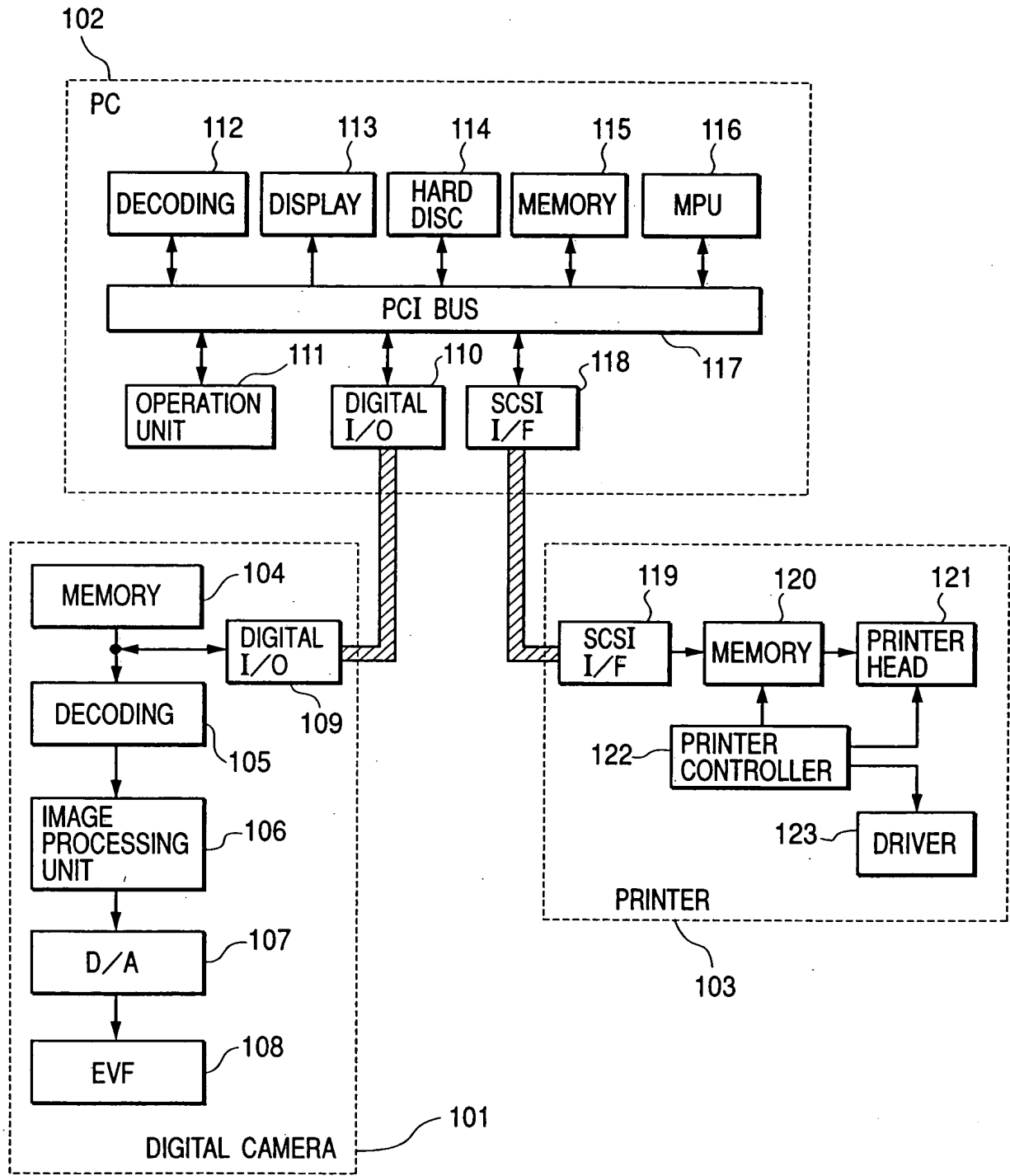


FIG. 2

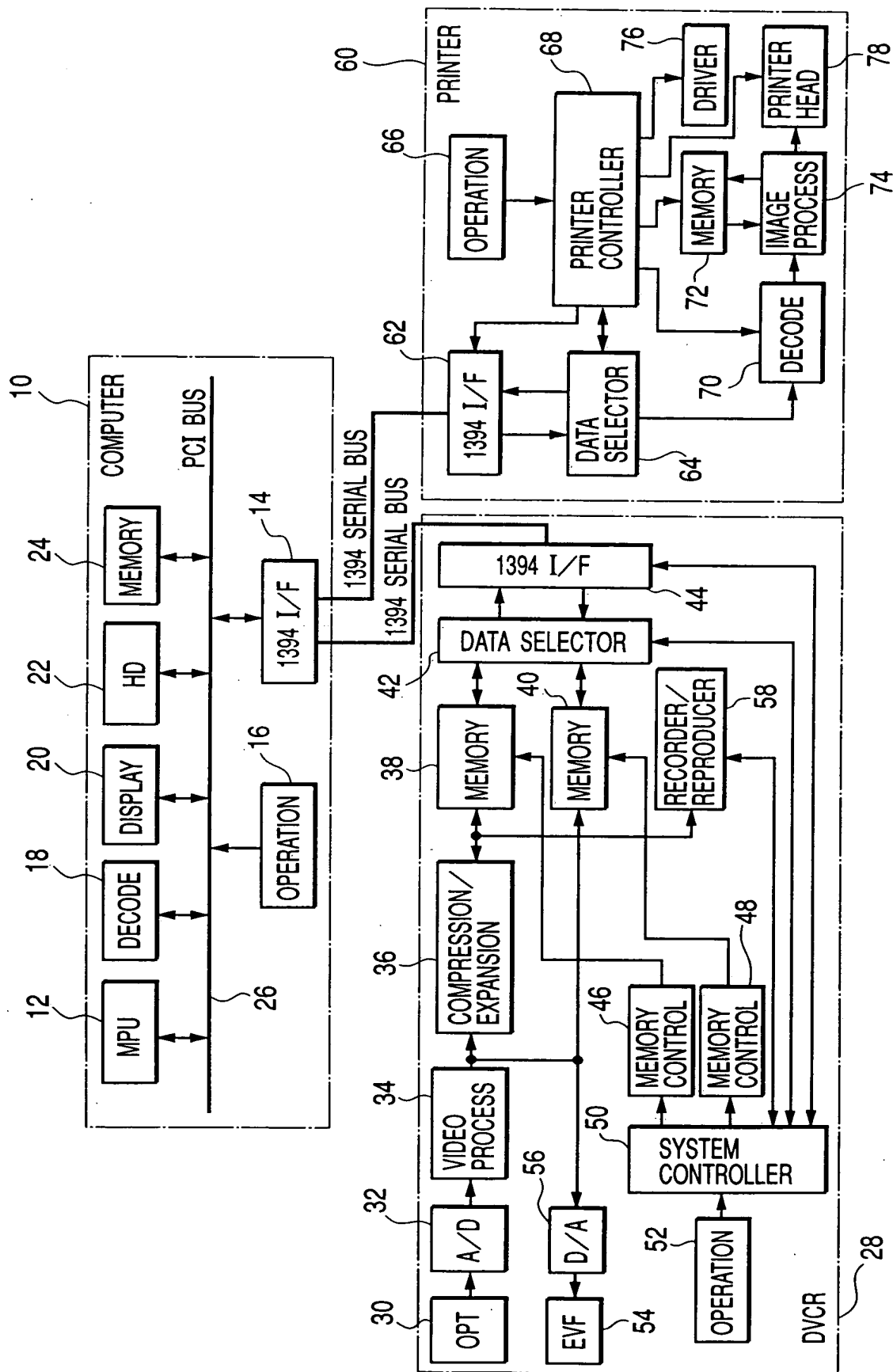
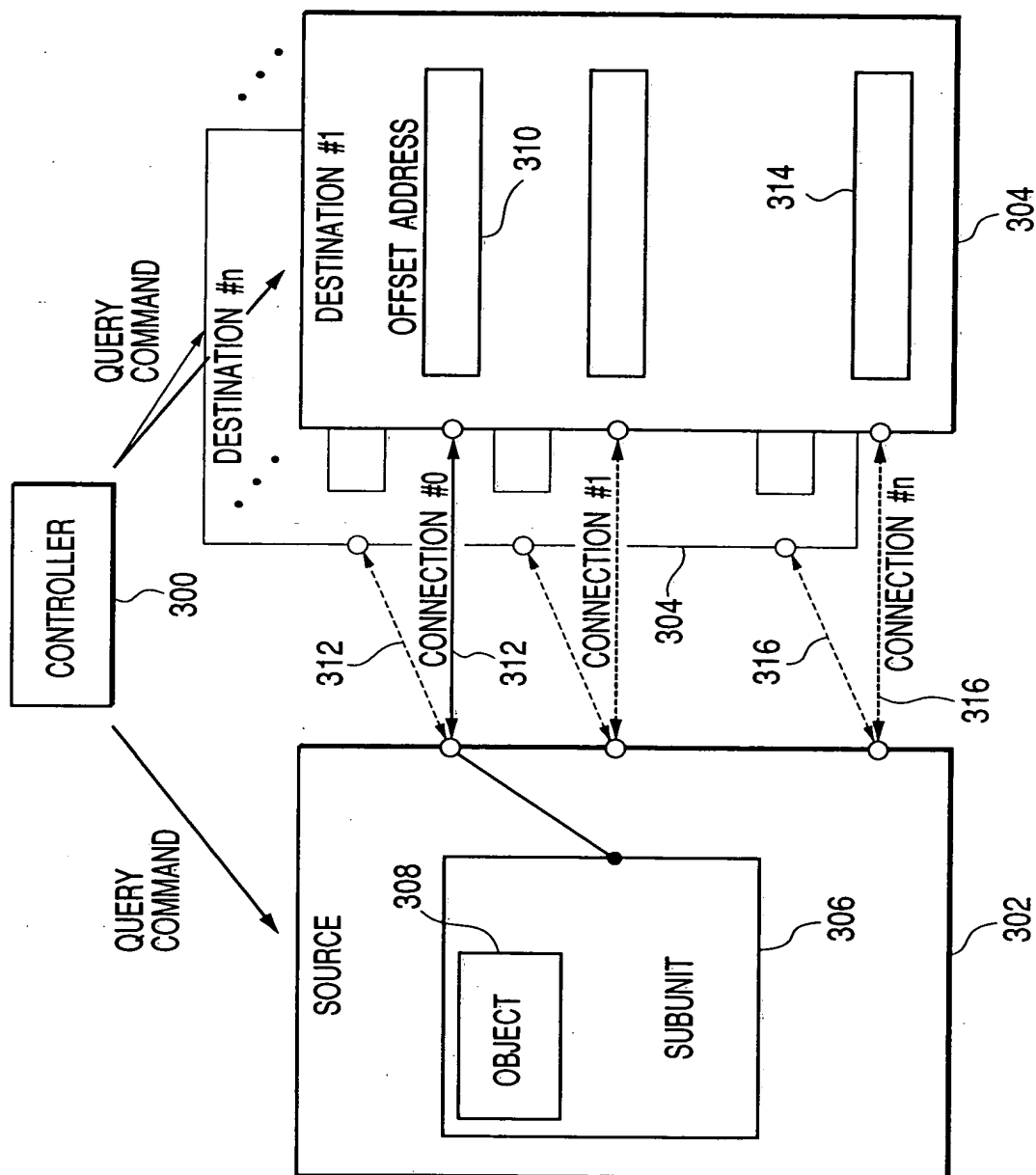
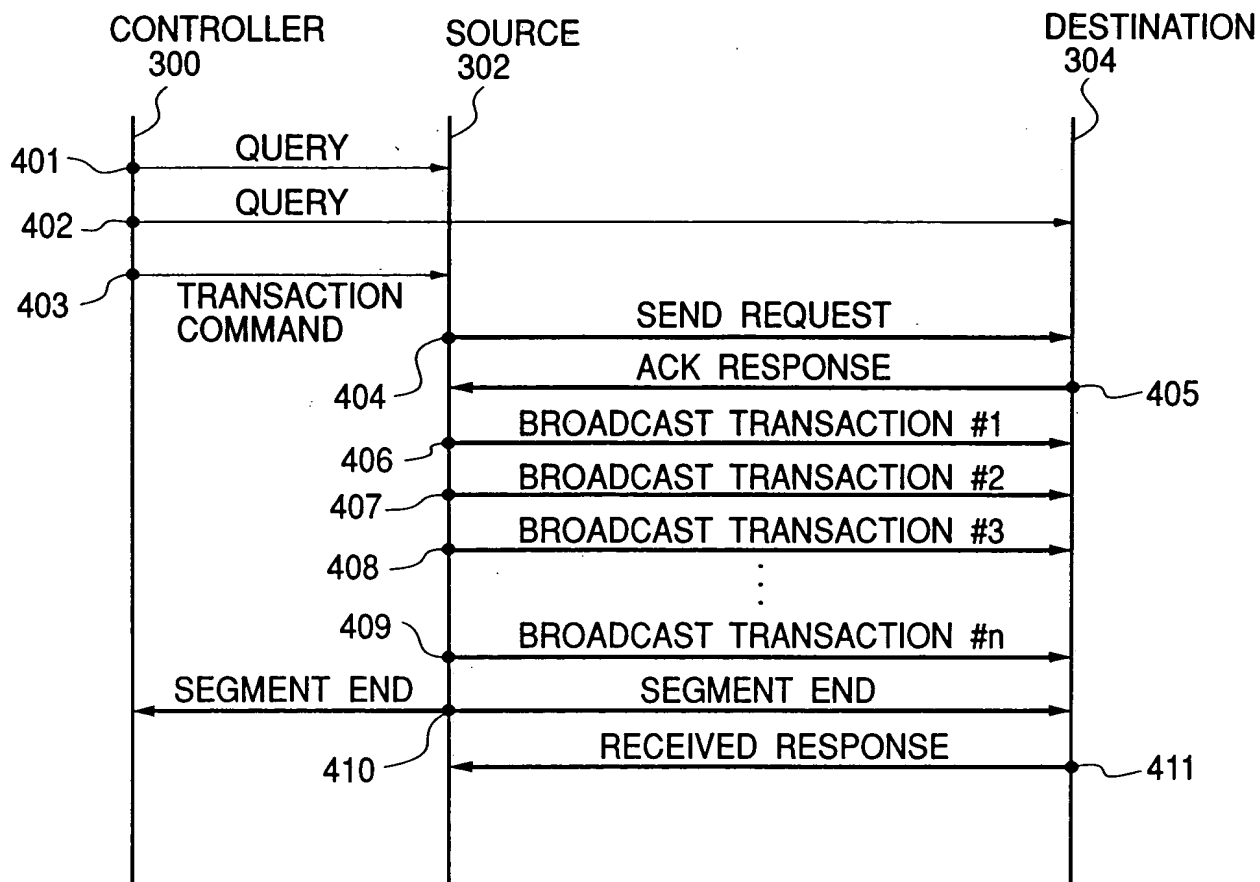


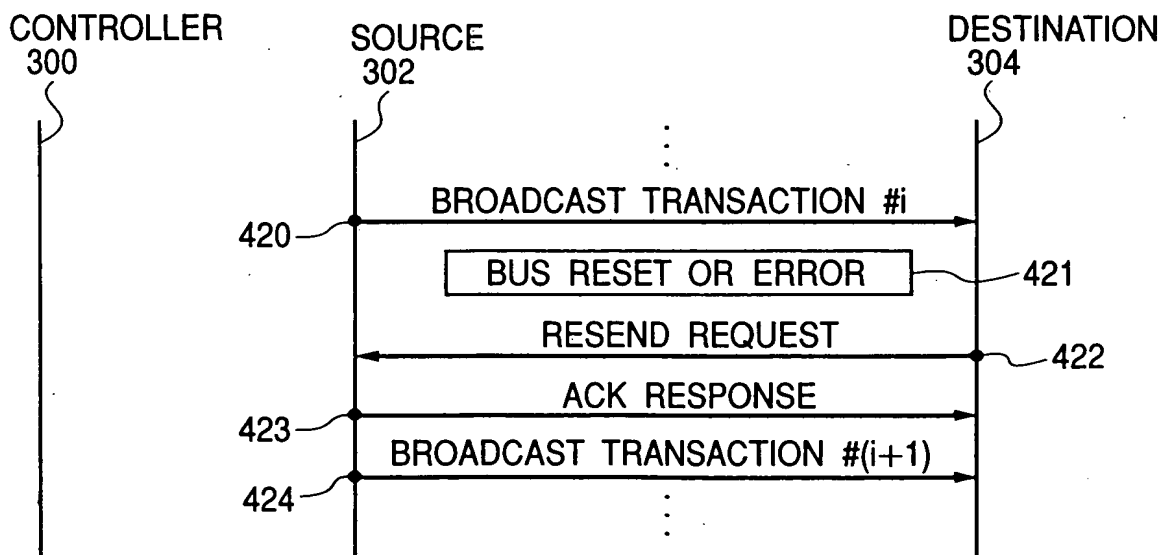
FIG. 3



**FIG. 4A**



**FIG. 4B**



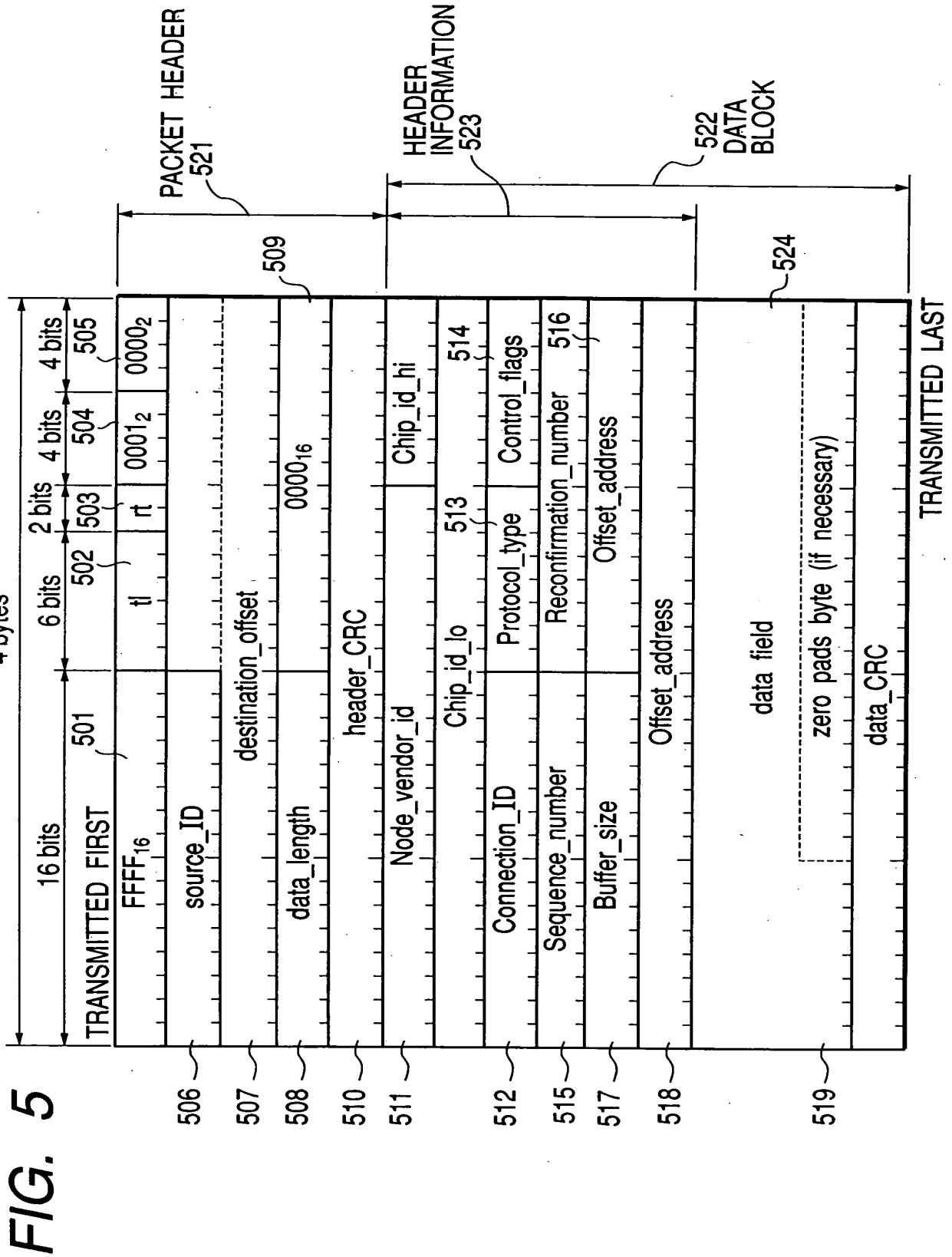
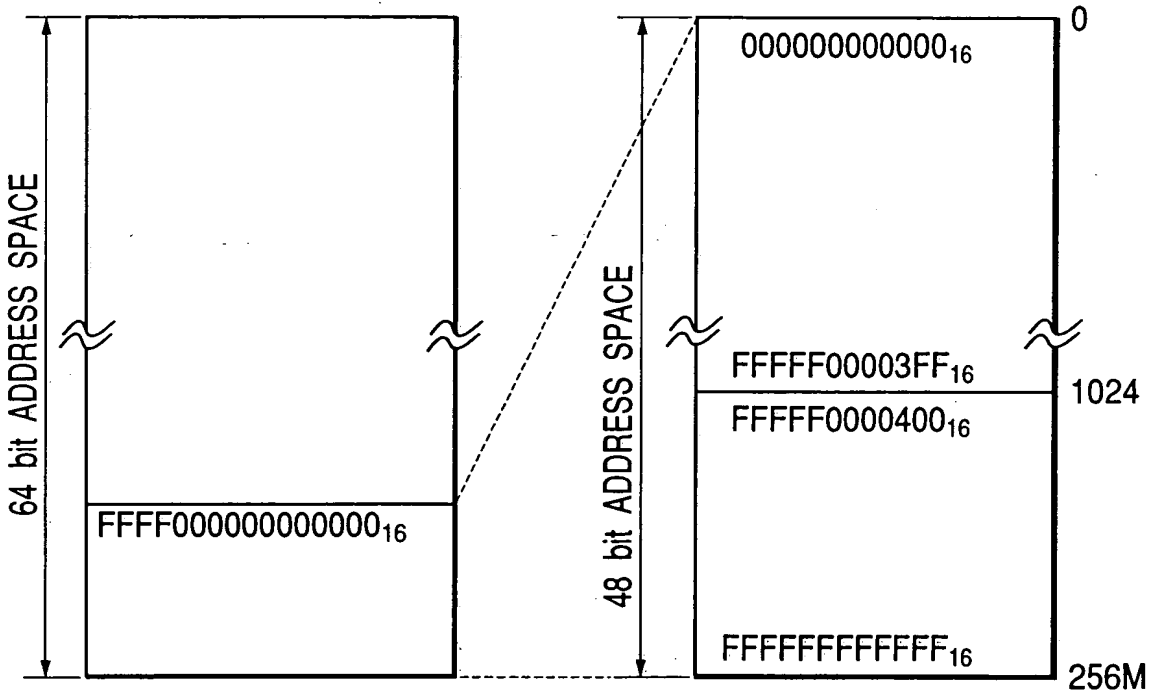


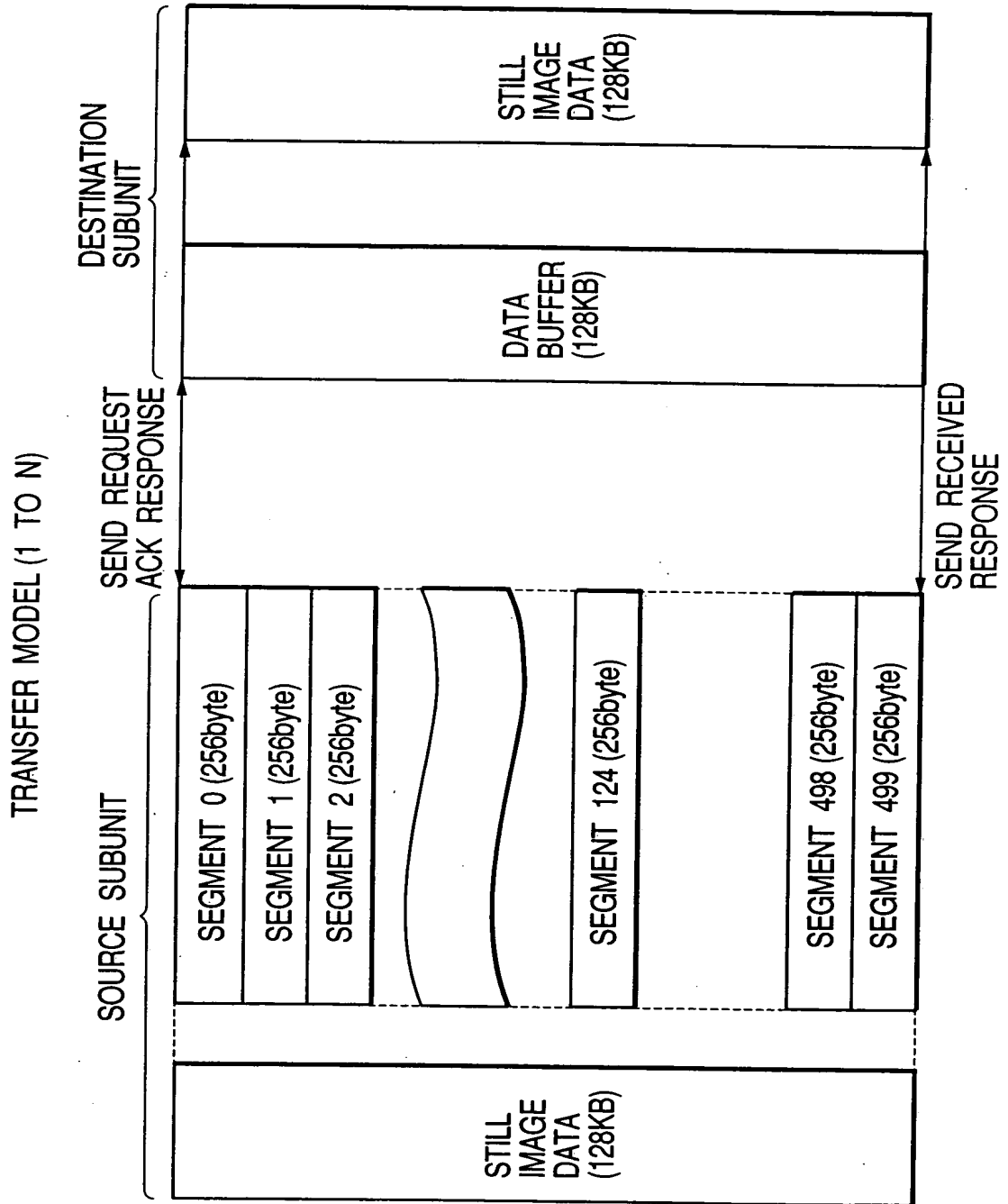
FIG. 6A

FIG. 6B

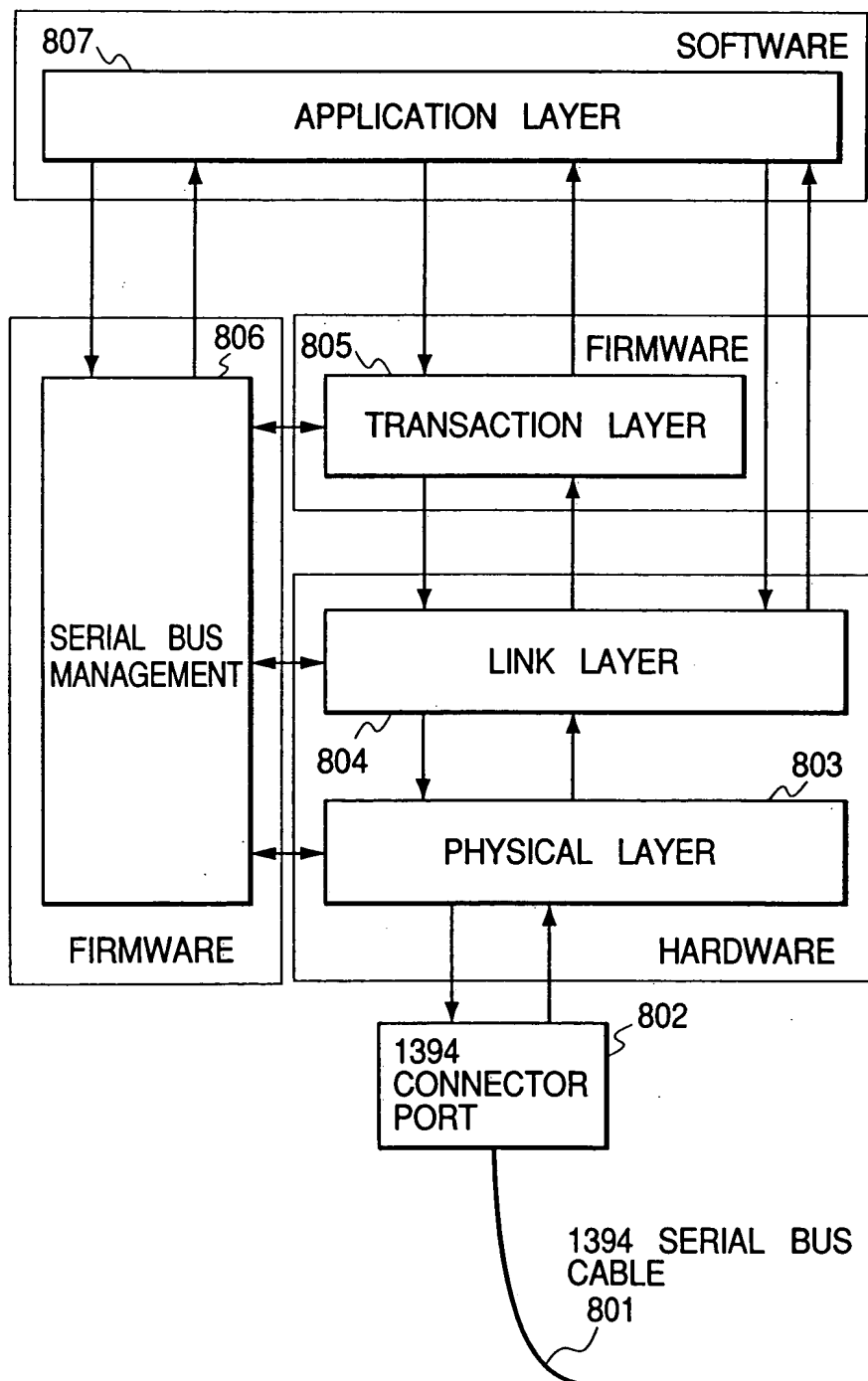


APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
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FIG. 7



**FIG. 8**





**FIG. 9**

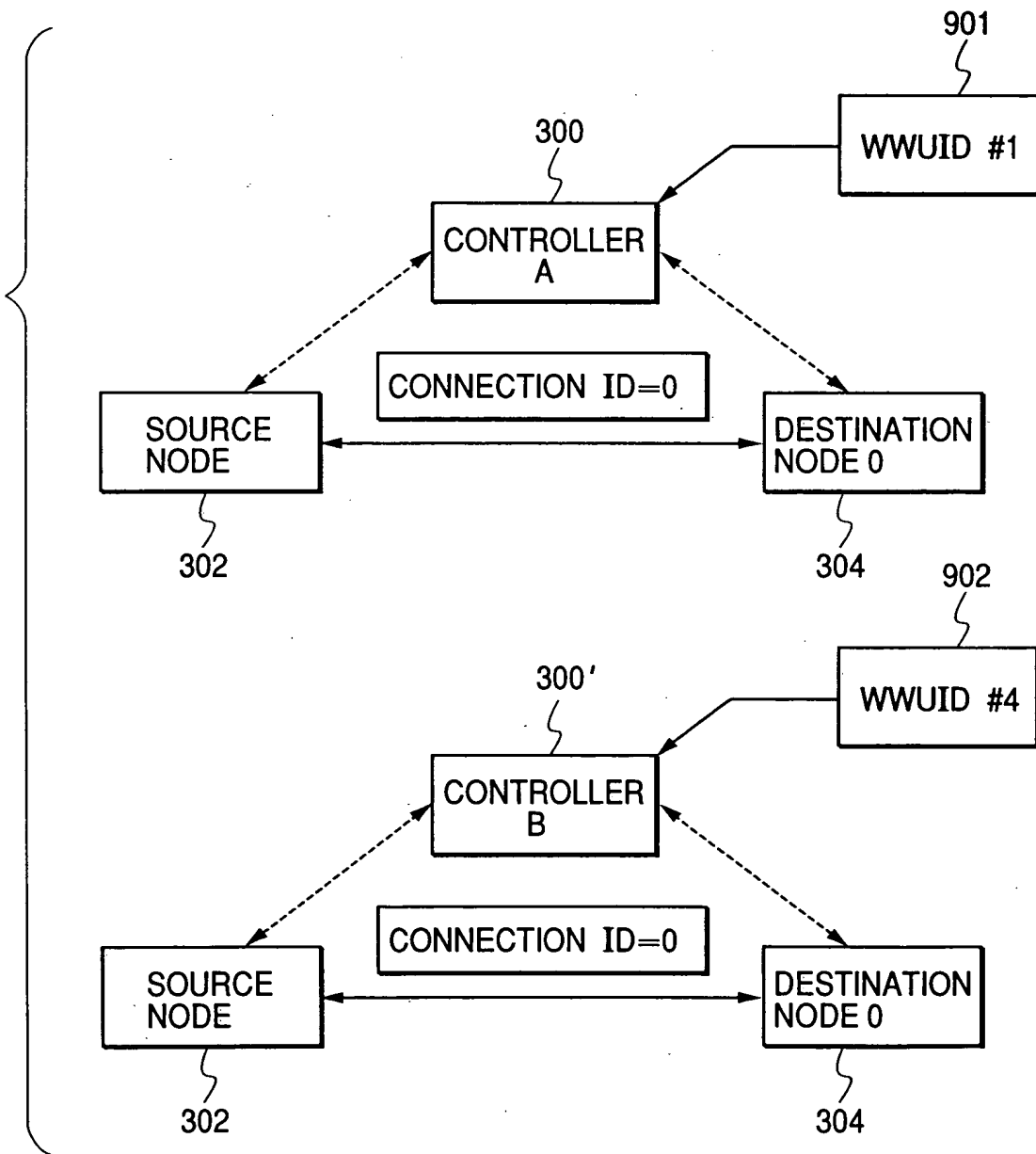
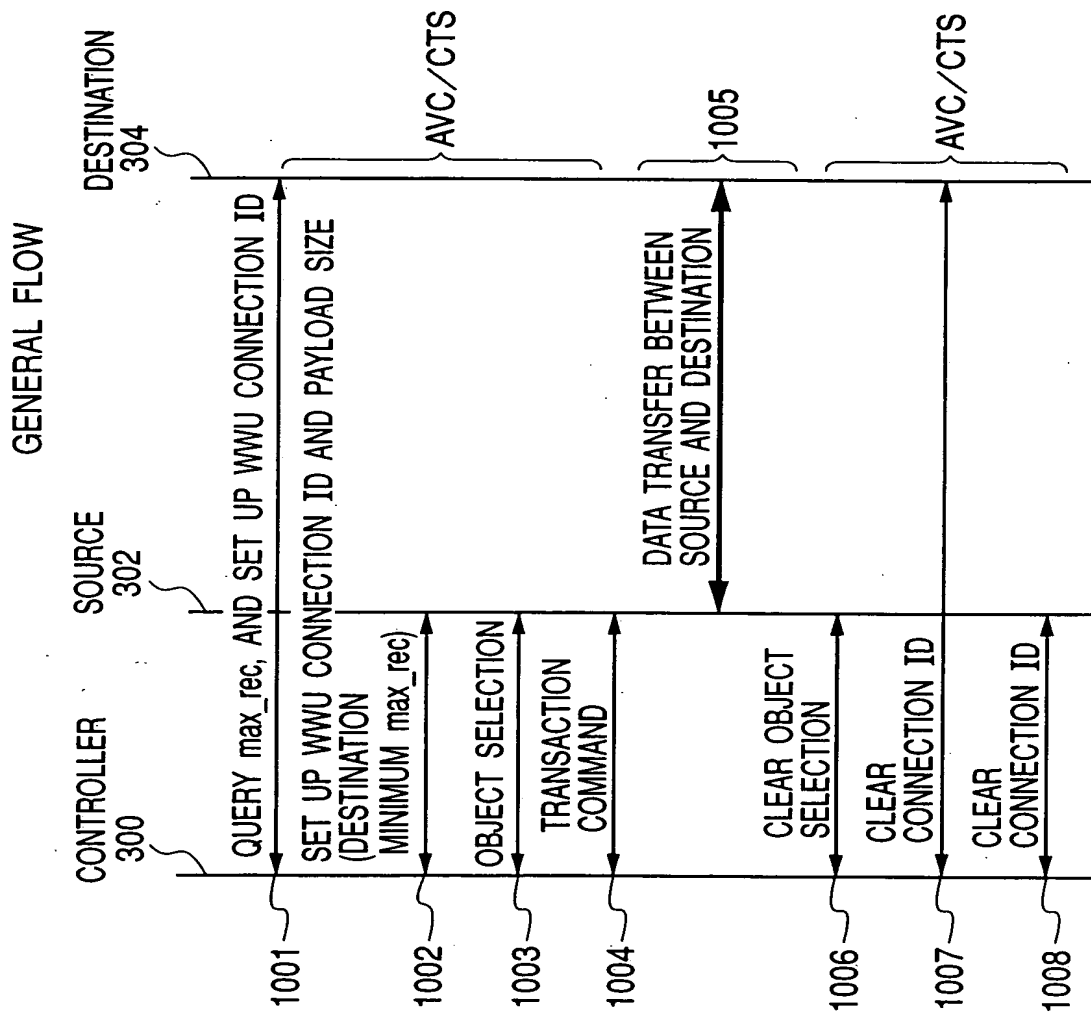


FIG. 10



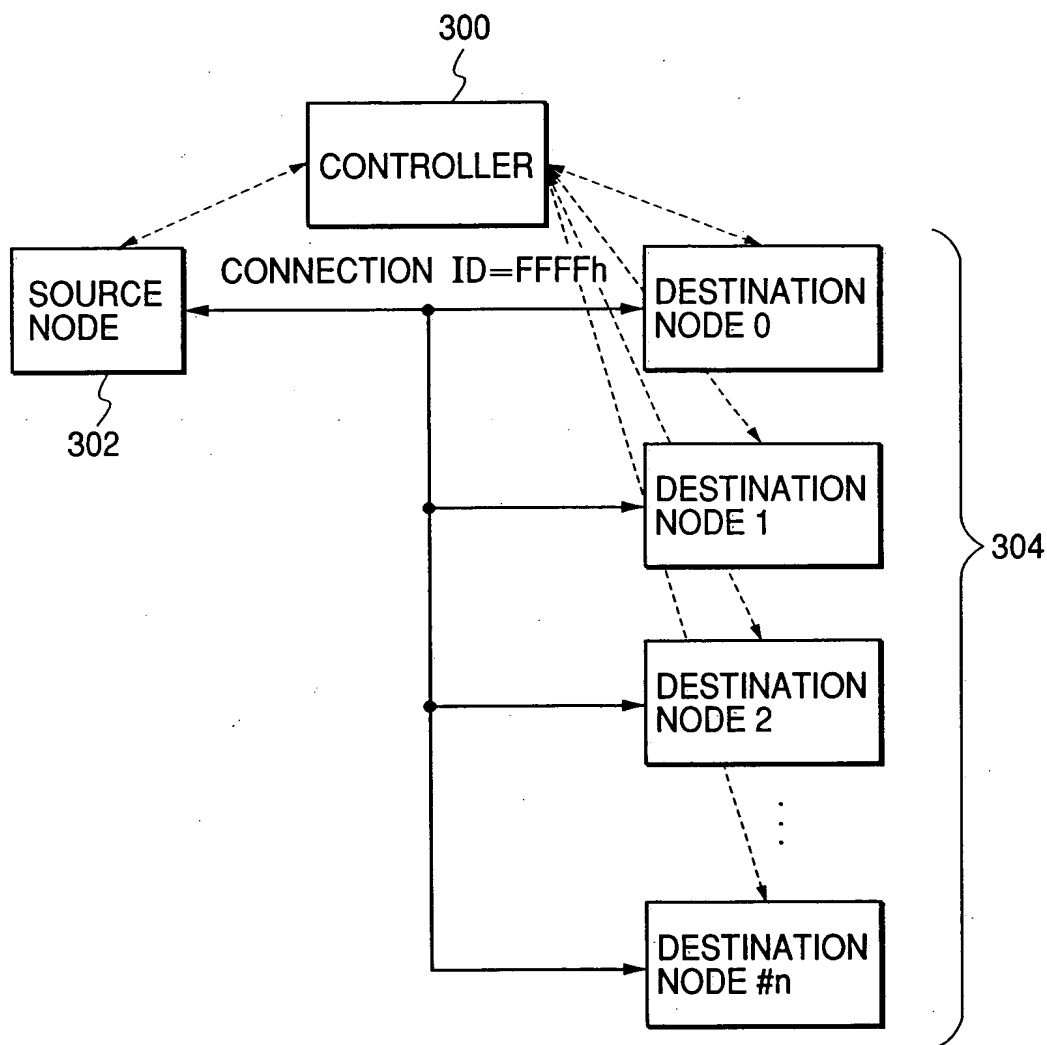
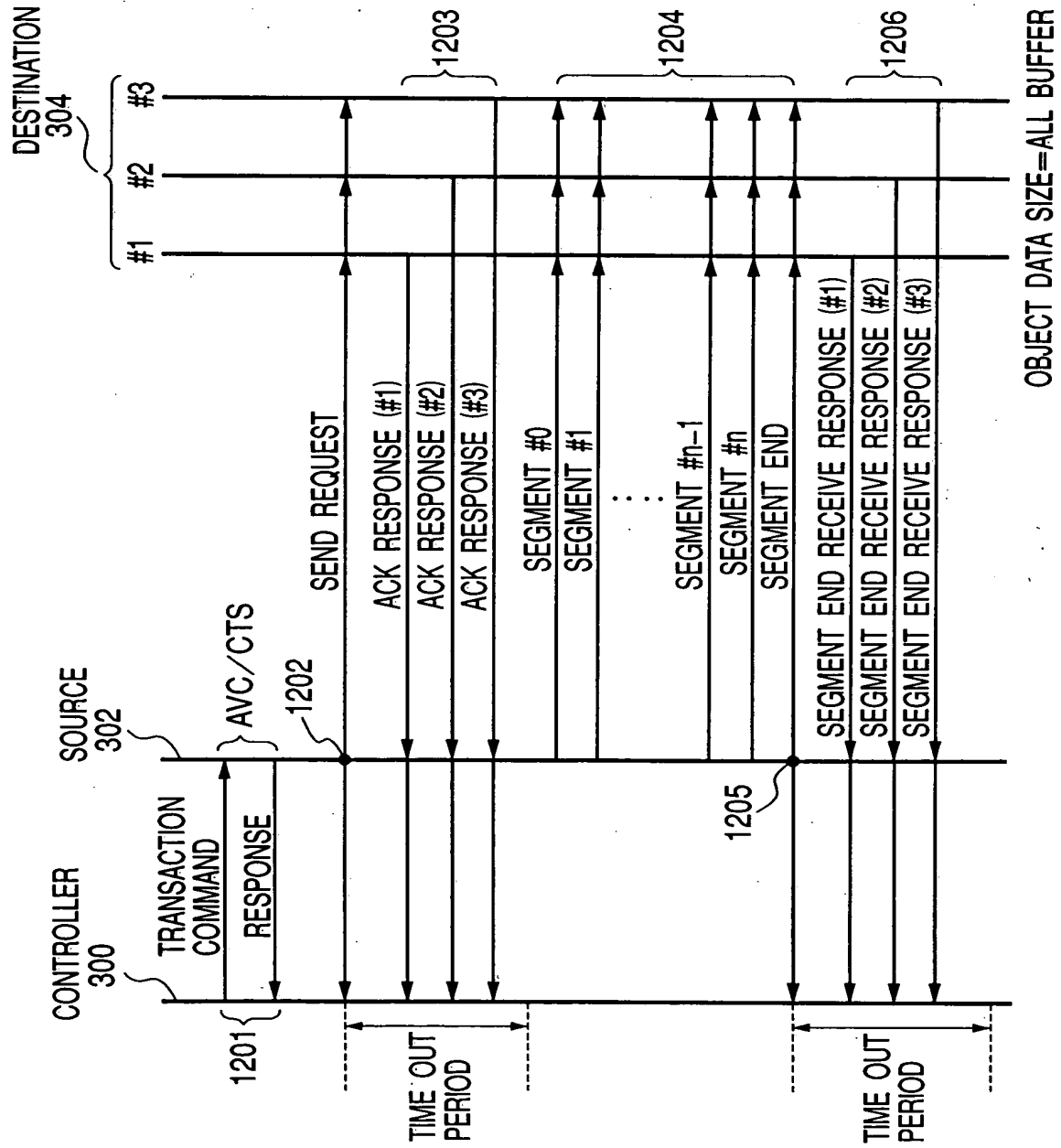
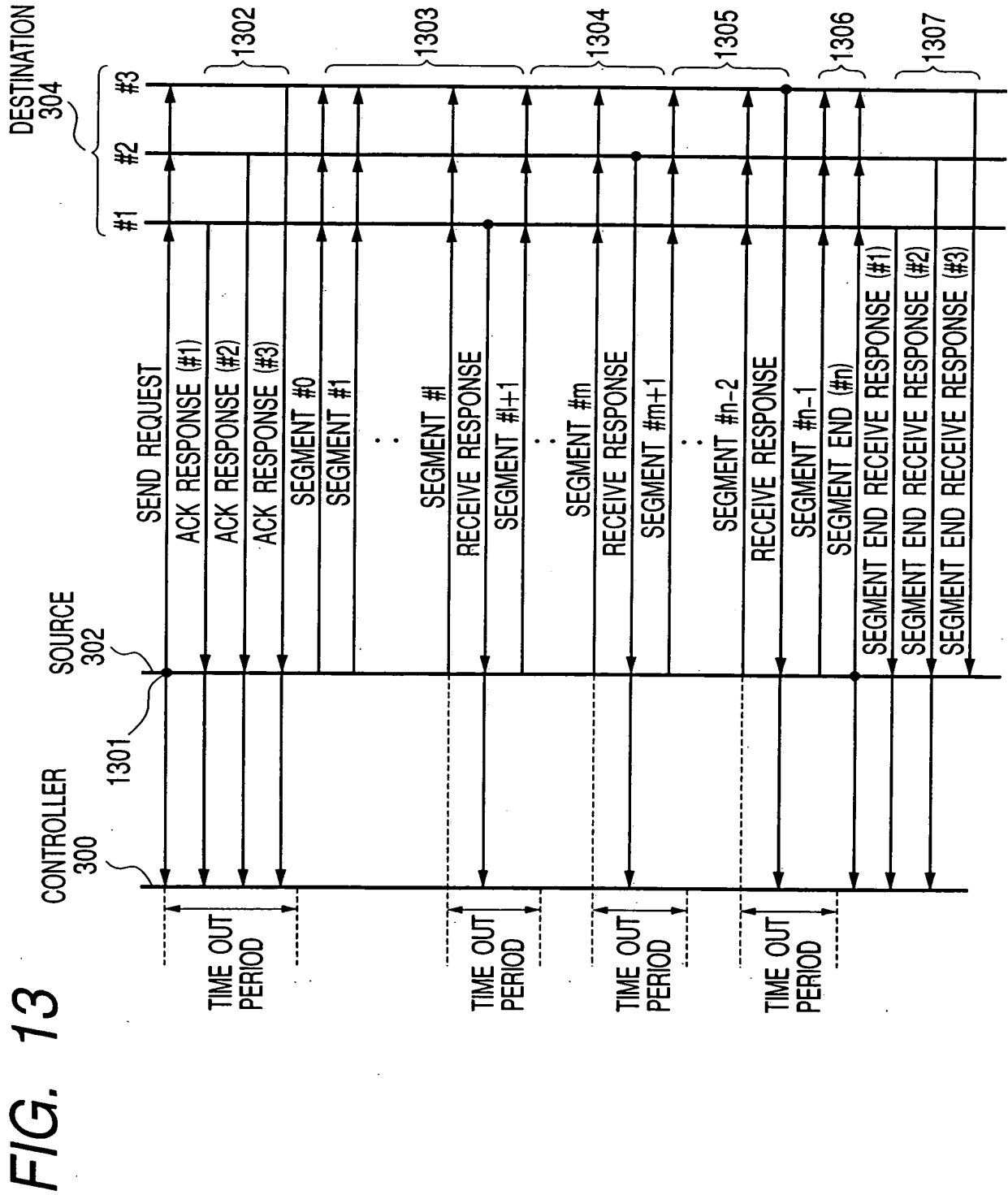
*FIG. 11*

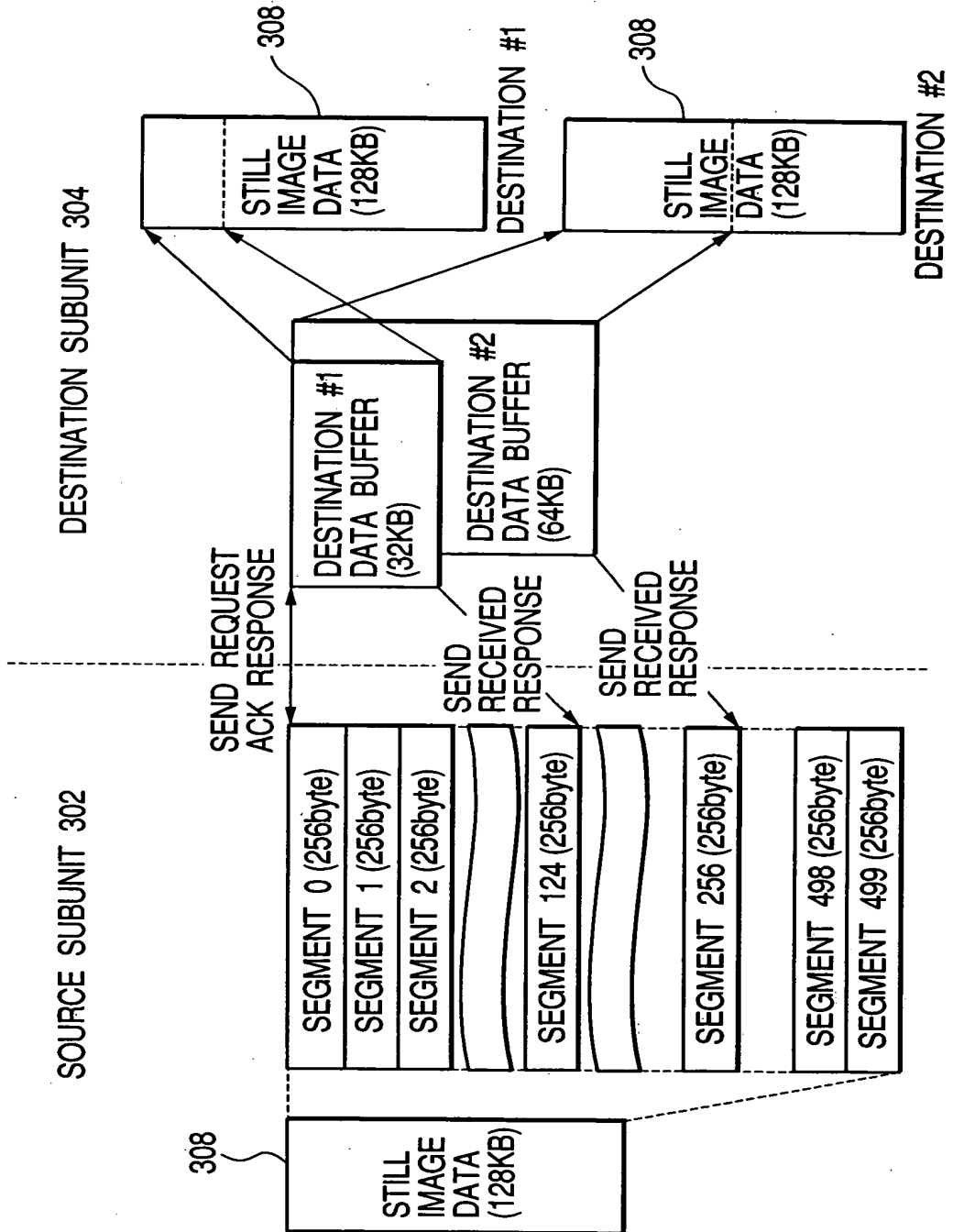
FIG. 12



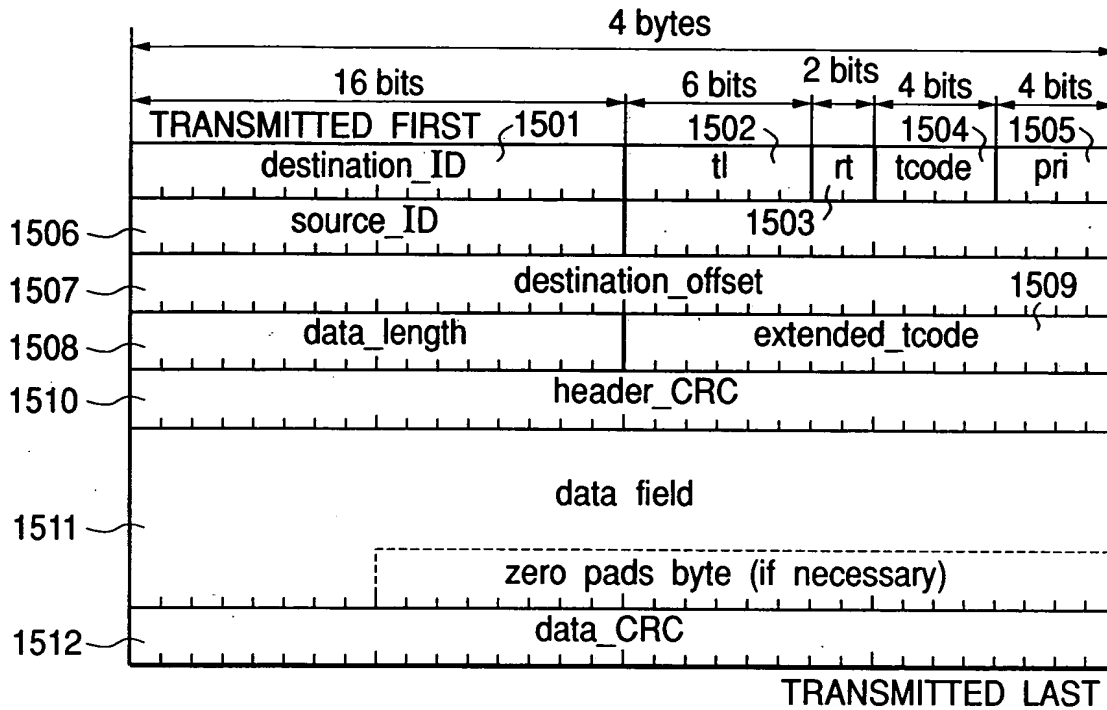


**FIG. 14**

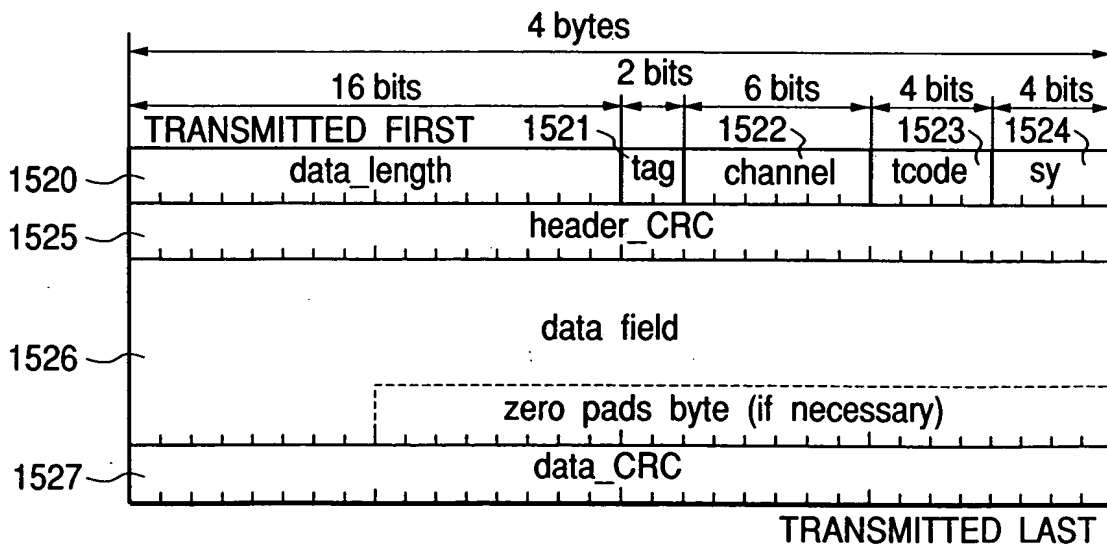
TRANSFER MODEL (1 TO N)



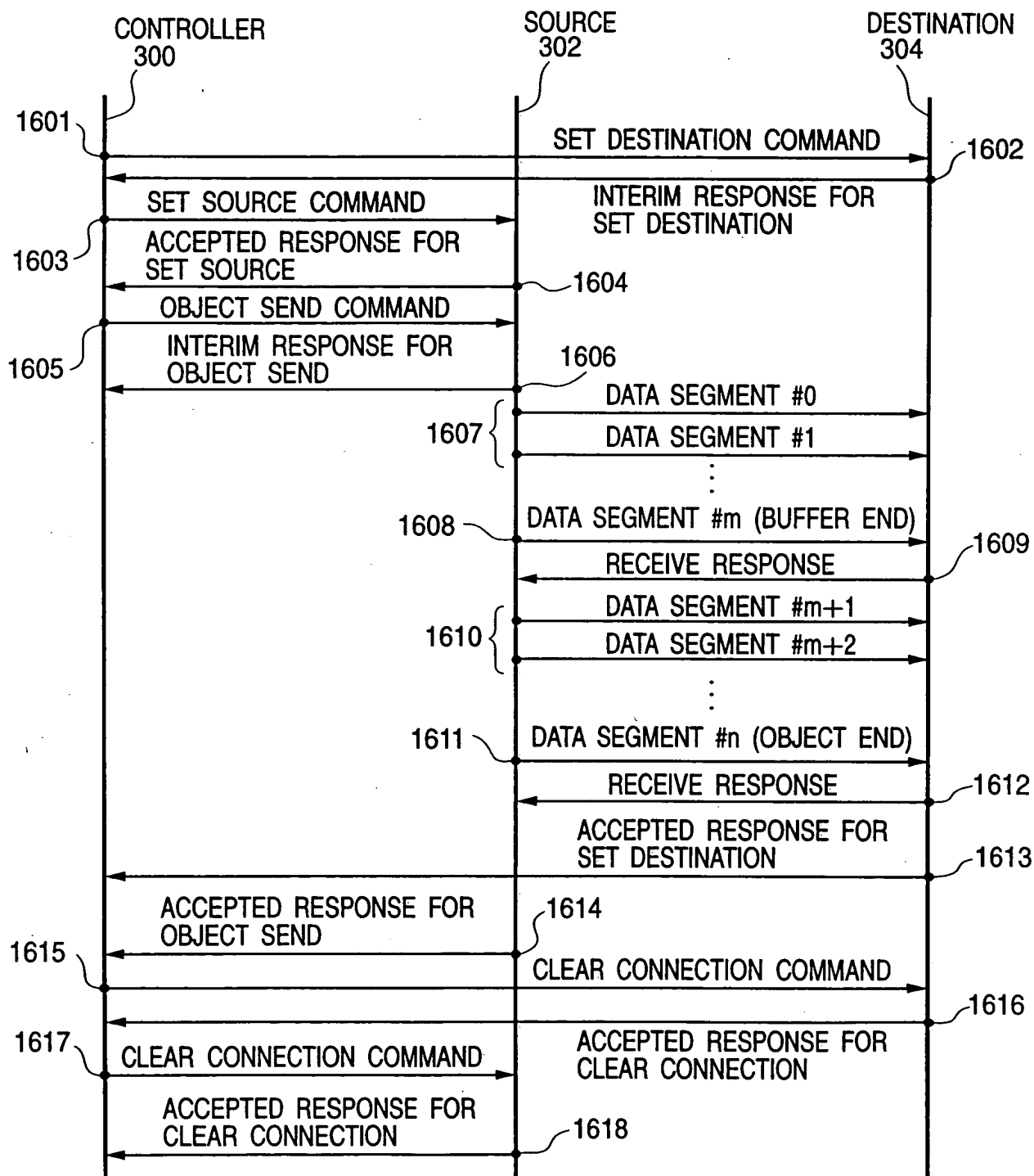
**FIG. 15A**



**FIG. 15B**



**FIG. 16**









APPROVED	O.G. FIG.	
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FIG. 21

	msb							lsb
opcode	OBJECT SEND							
operand [0]	node_vendor_id							
operand [1]								
operand [2]								
operand [3]	chip_id_hi							
operand [4]	chip_id_lo							
operand [5]								
operand [6]								
operand [7]								
operand [8]	connection_id							
operand [9]	subfunction							
operand [10]	status_info							

FIG. 22

connection_id	flag	max_rec	buffer_size
0	0	512	32KB
0	0	0	0
⋮			
n-1	0	0	0
n	0	0	0

**FIG. 23**

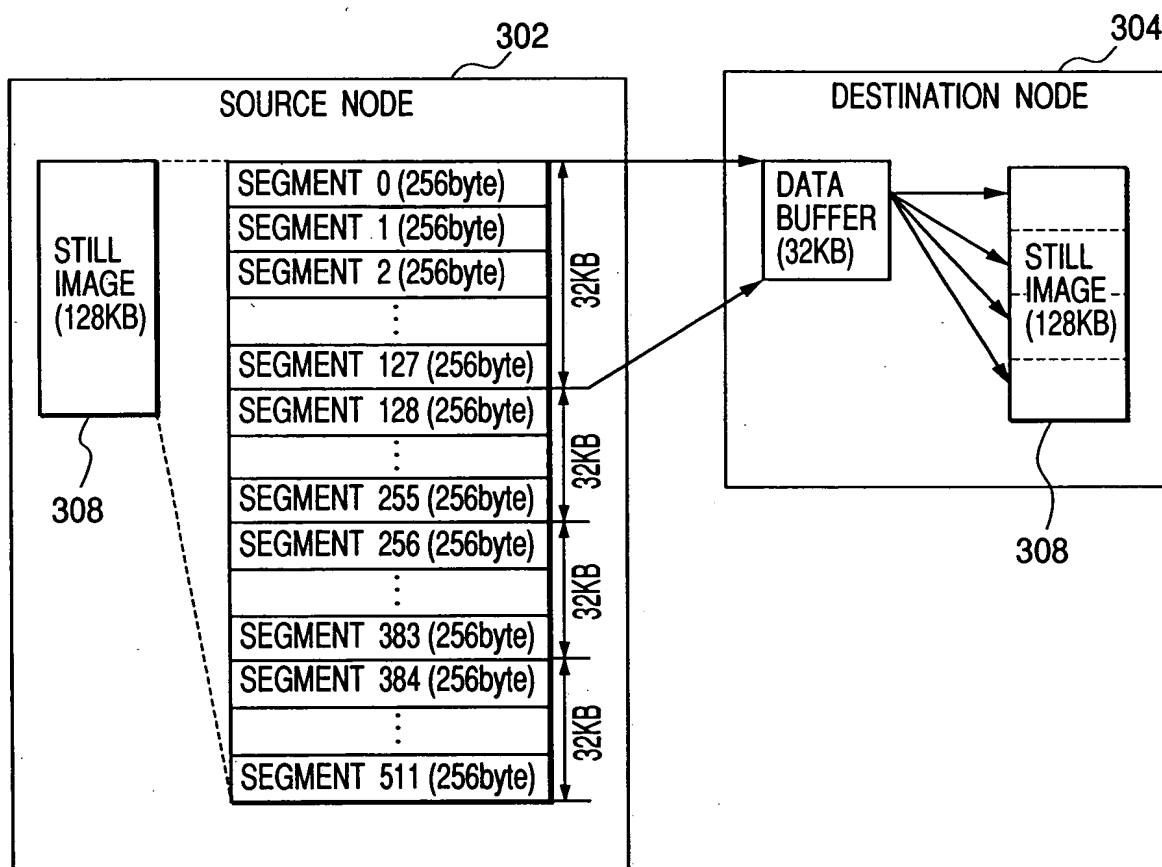




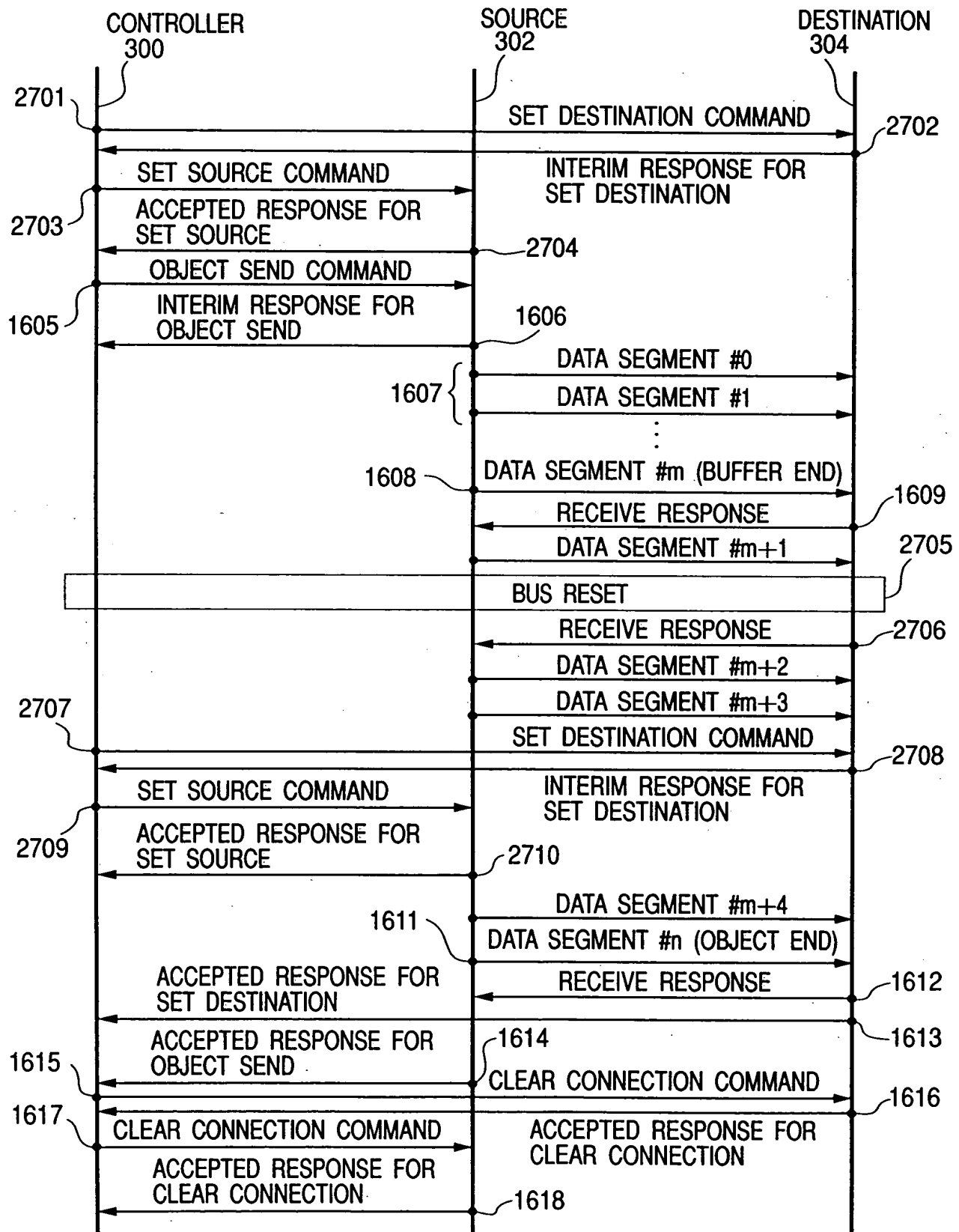
FIG. 26

	msb							lsb
opcode	CLEAR CONNECTION							
operand [0]	node_vendor_id							
operand [1]								
operand [2]								
operand [3]	chip_id_hi							
operand [4]	chip_id_lo							
operand [5]								
operand [6]								
operand [7]								
operand [8]	connection_id							
operand [9]	status_info							

FIG. 30

connection_id	flag	max_rec	buffer_size	number_of_destinations	→	EUI-64	
0	1	512	32KB	1		Source	xxxxxx xx xxxxxxxx
1	0	0	0	0		Destination #1	yyyyyy yy yyyyyyyy
n-1	0	0	0	0			
n	0	0	0	0			

**FIG. 27**





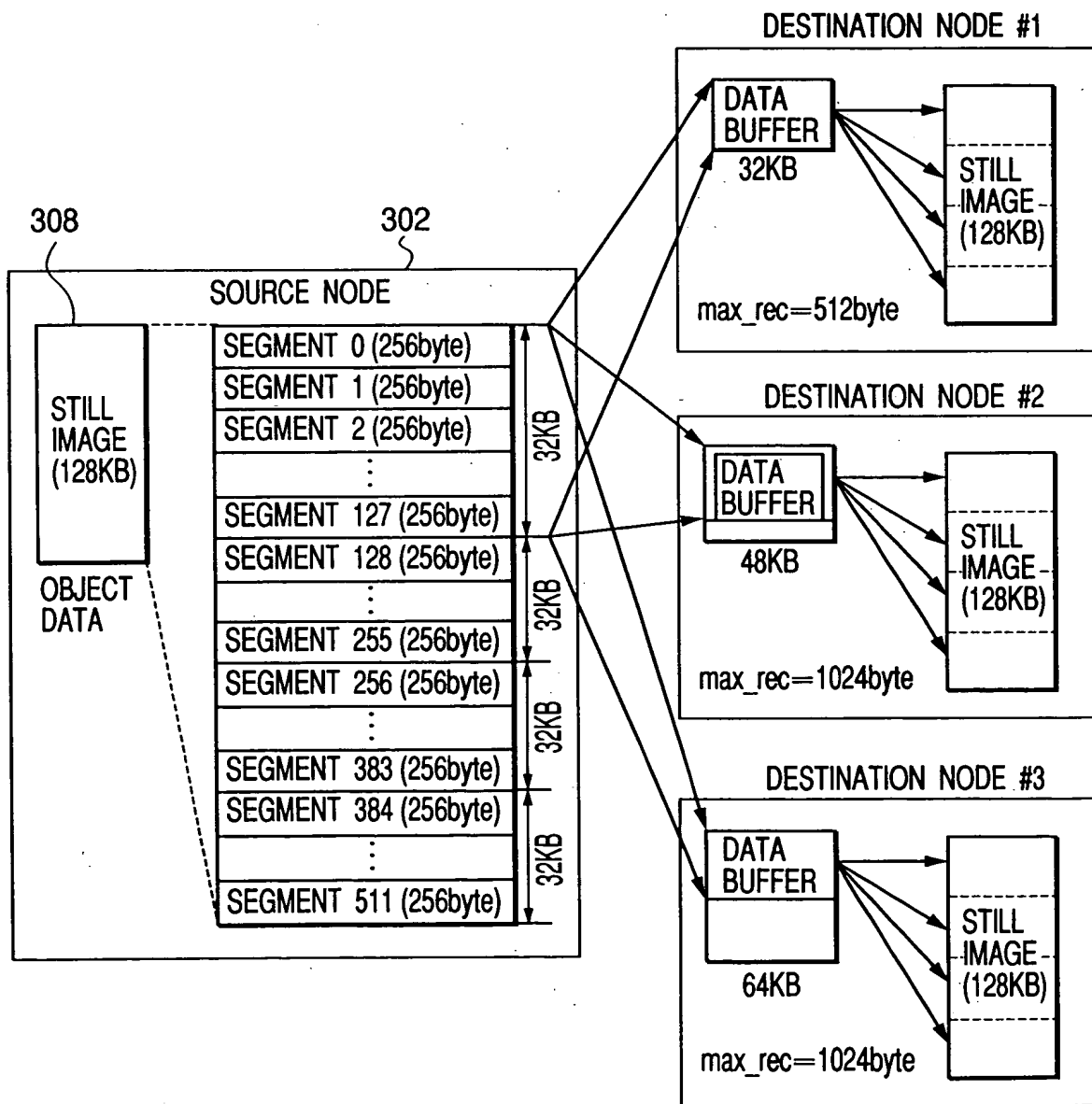


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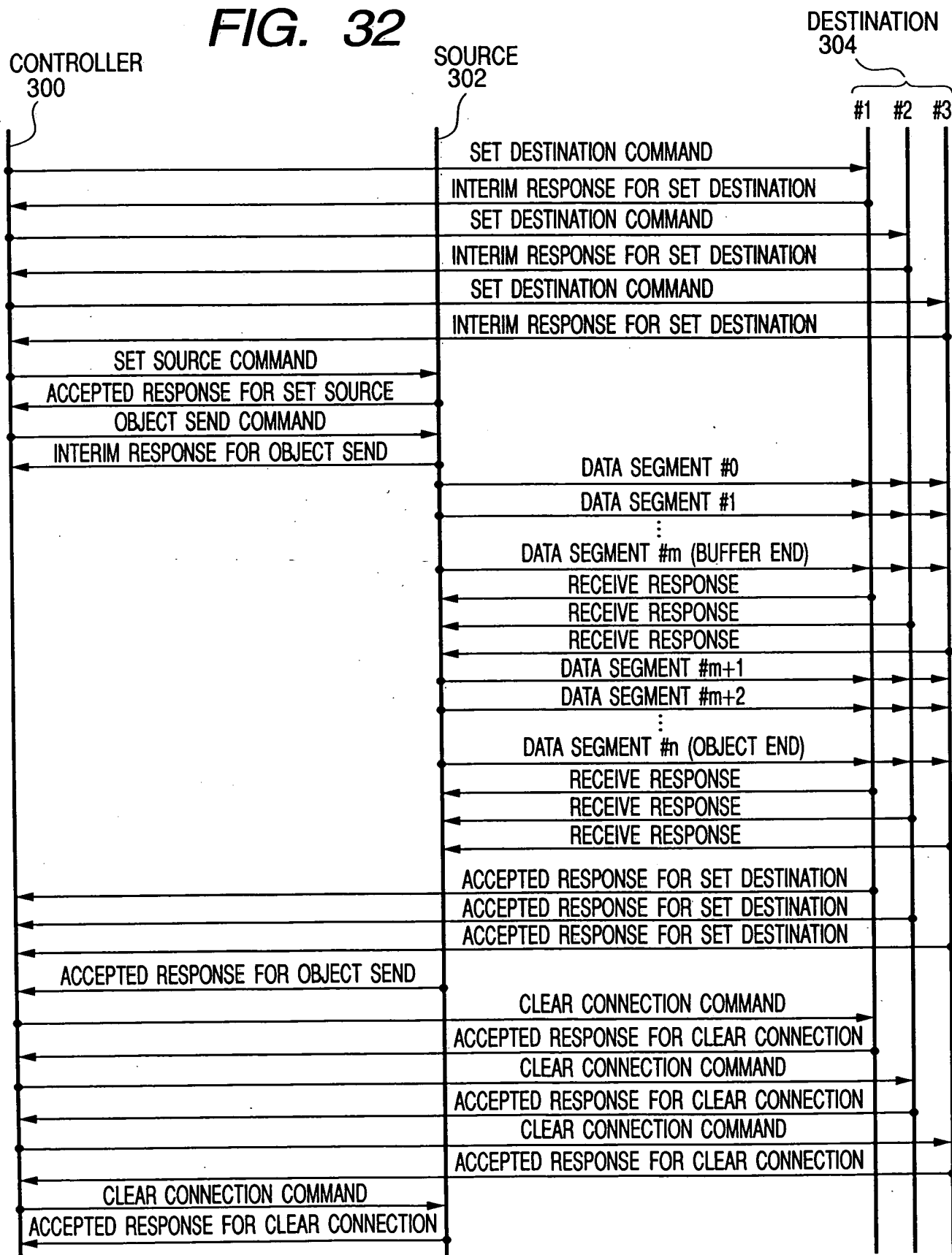
**FIG. 29**

msb								lsb
opcode	SET SOURCE							
operand [0]	node_vendor_id							
operand [1]								
operand [2]								
operand [3]	chip_id_hi							
operand [4]	chip_id_lo							
operand [5]								
operand [6]								
operand [7]								
operand [8]	connection_id							
operand [9]	source_node_vendor_id							
operand [10]								
operand [11]								
operand [12]	source_chip_id_hi							
operand [13]	source_chip_id_lo							
operand [14]								
operand [15]								
operand [16]								
operand [17]	reserved				max_rec			
operand [18]	buffer_size							
operand [19]								
operand [20]								
operand [21]	number_of_destinations							
operand [22]	status_info							

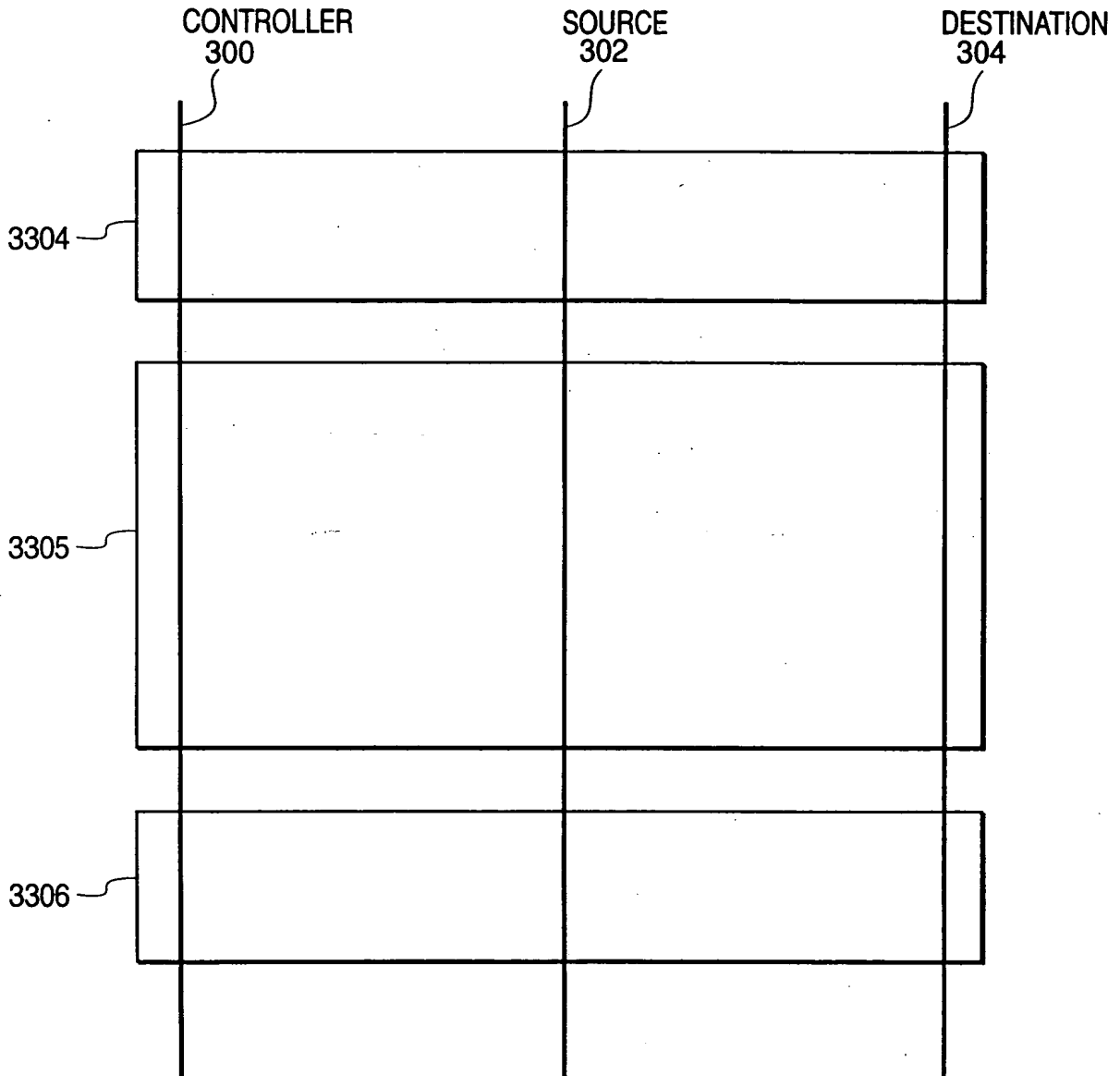
**FIG. 31**



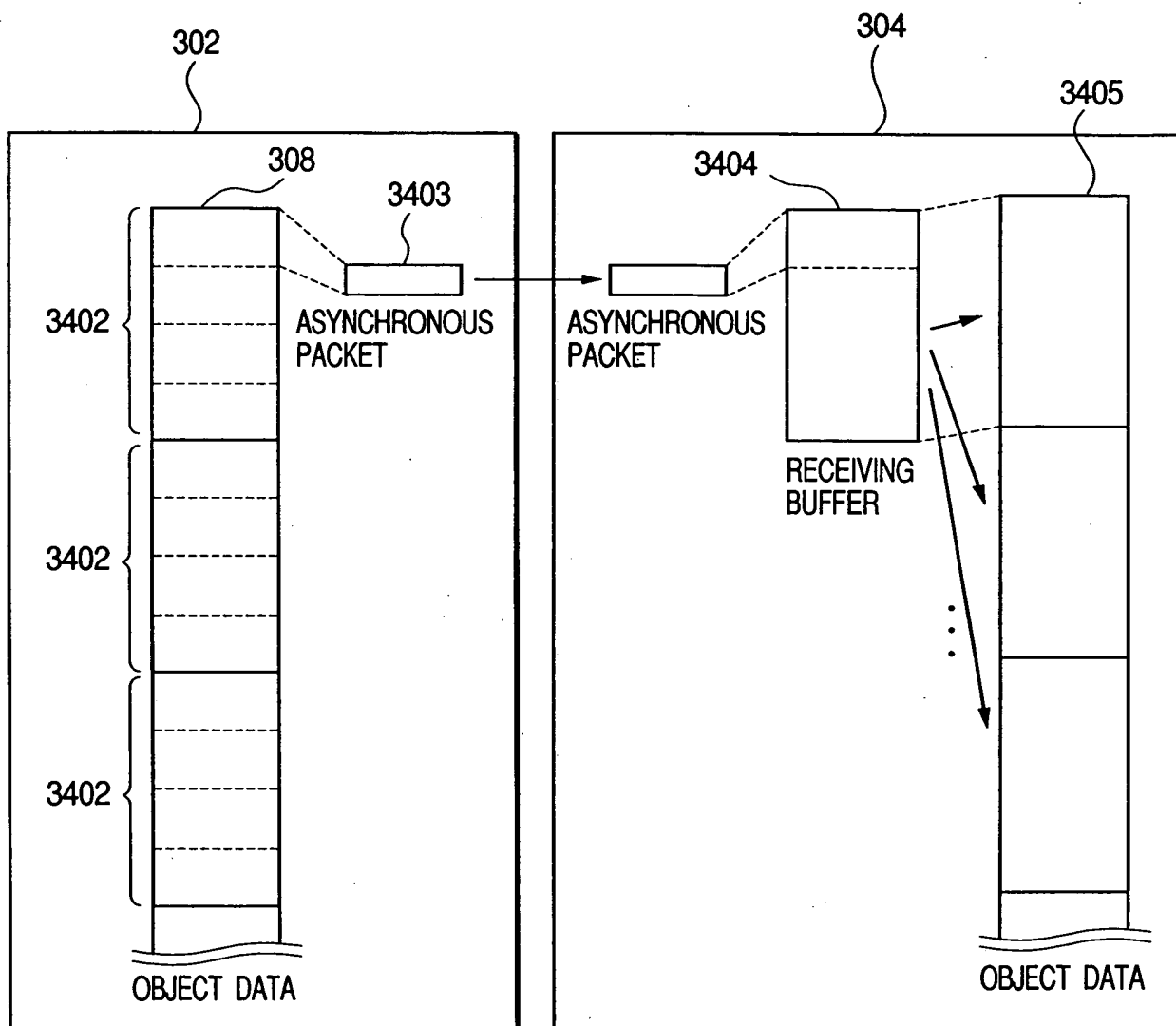
**FIG. 32**



*FIG. 33*



**FIG. 34**





APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
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*FIG. 36*

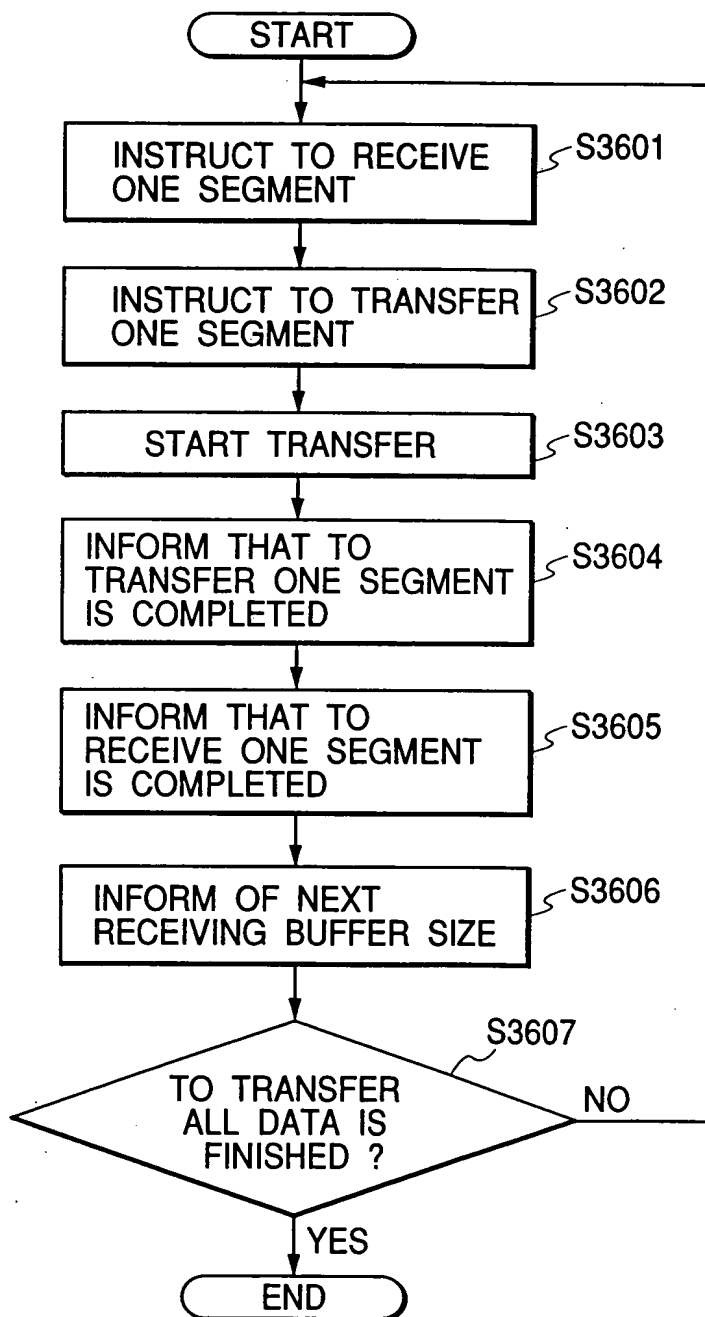


FIG. 37

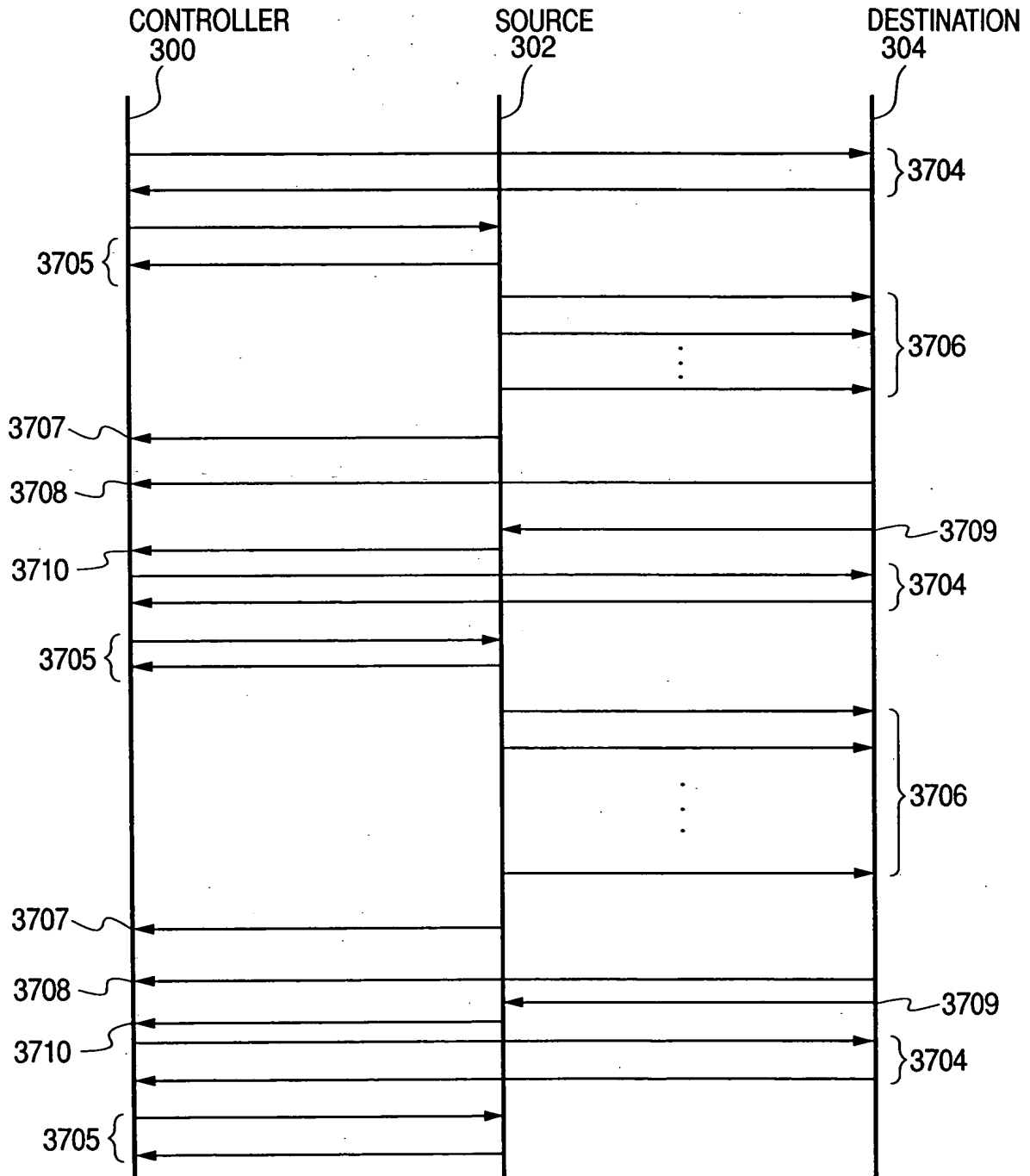
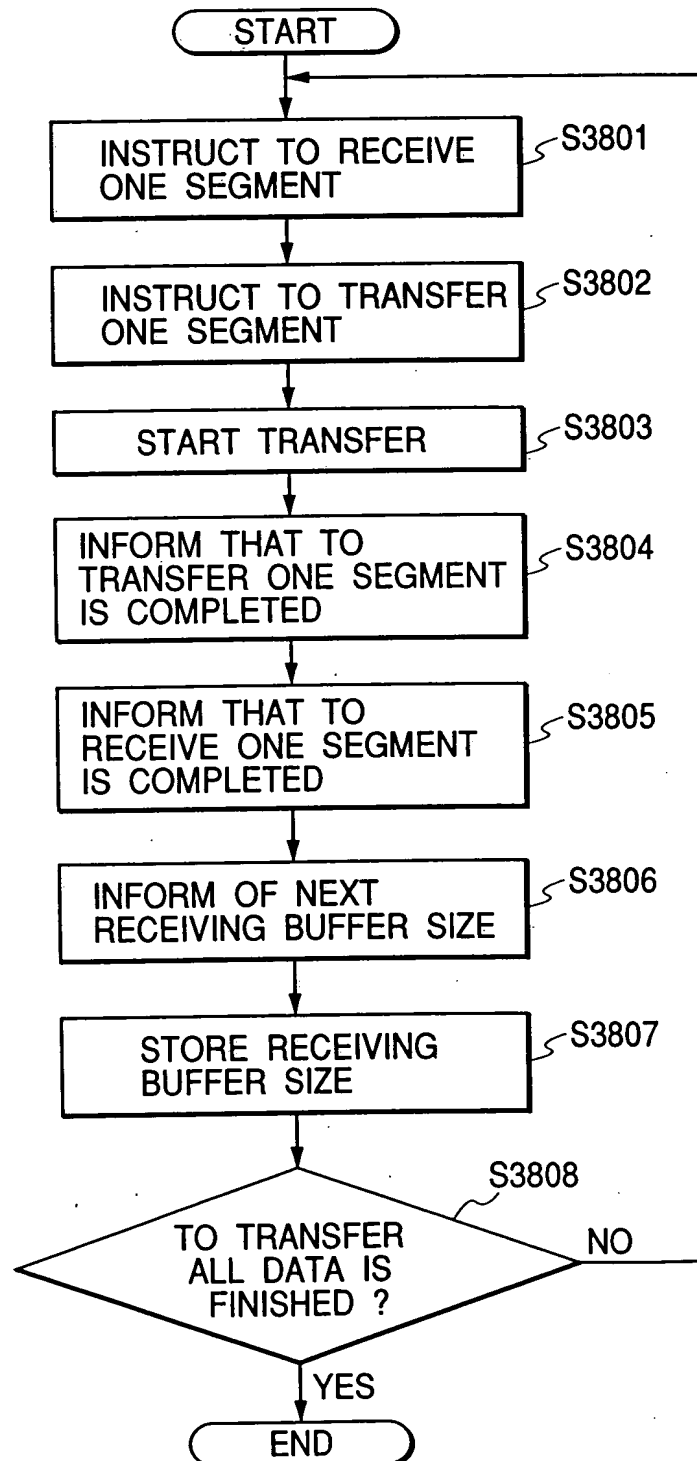
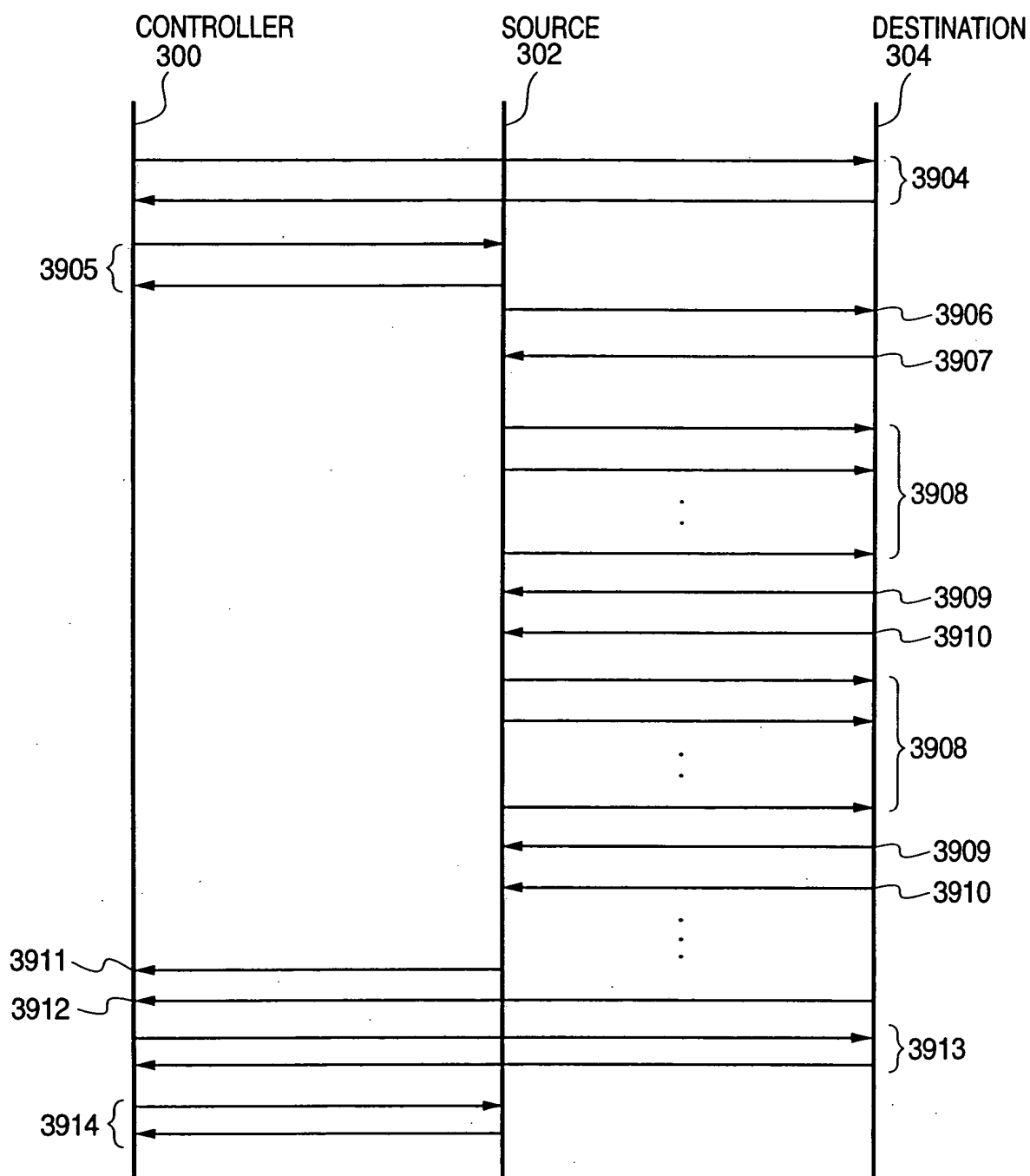




FIG. 38



**FIG. 39**



APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
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FIG. 40

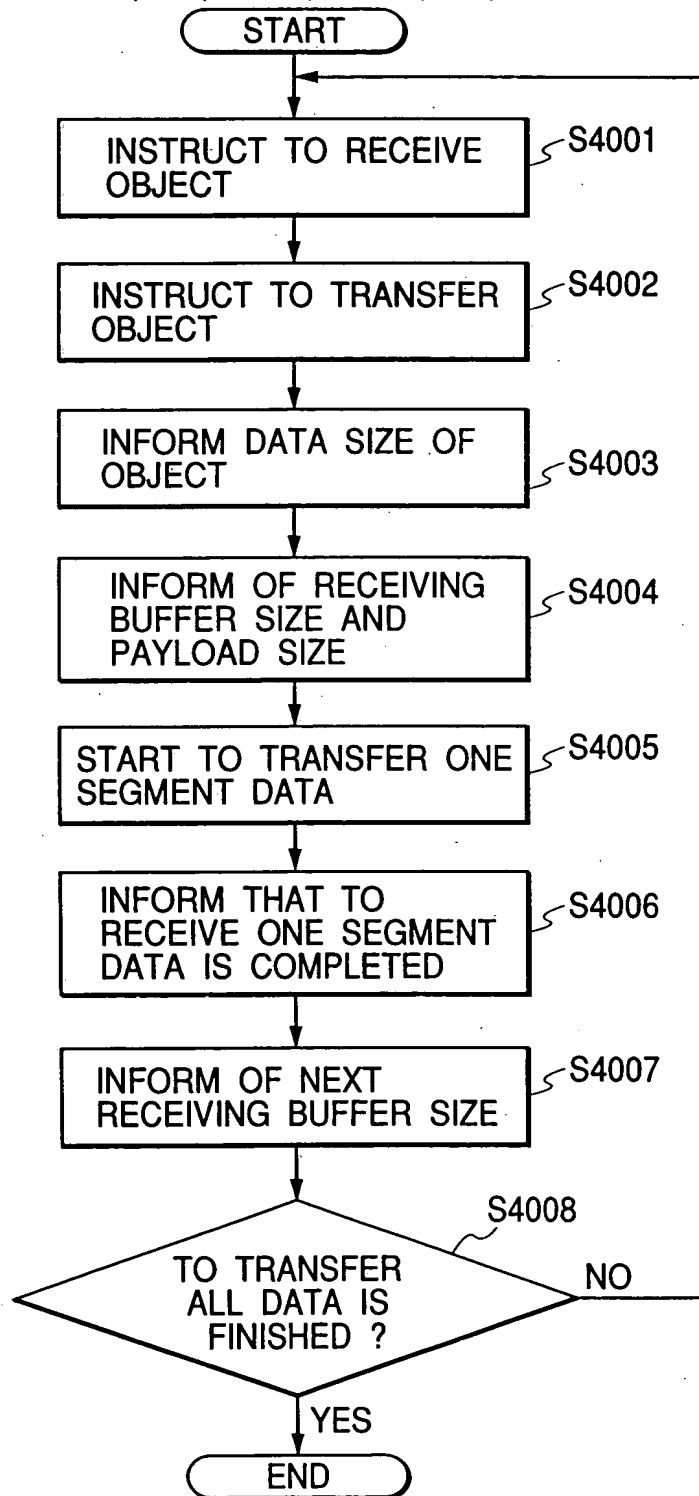
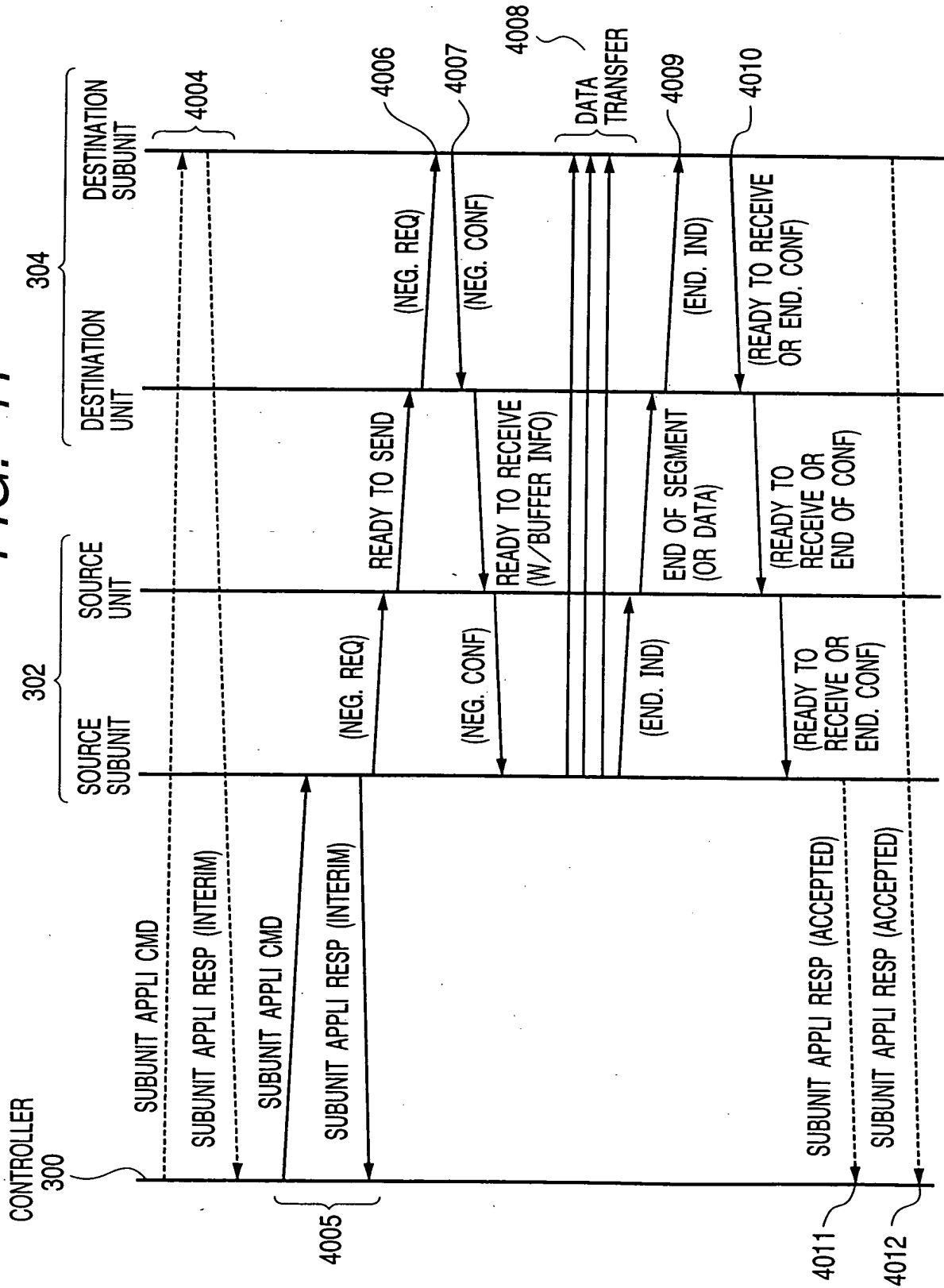


FIG. 41



# FIG. 42

BUFFER MODEL 1 SOURCE BUFFER 1 => DEST. BUFFER 1

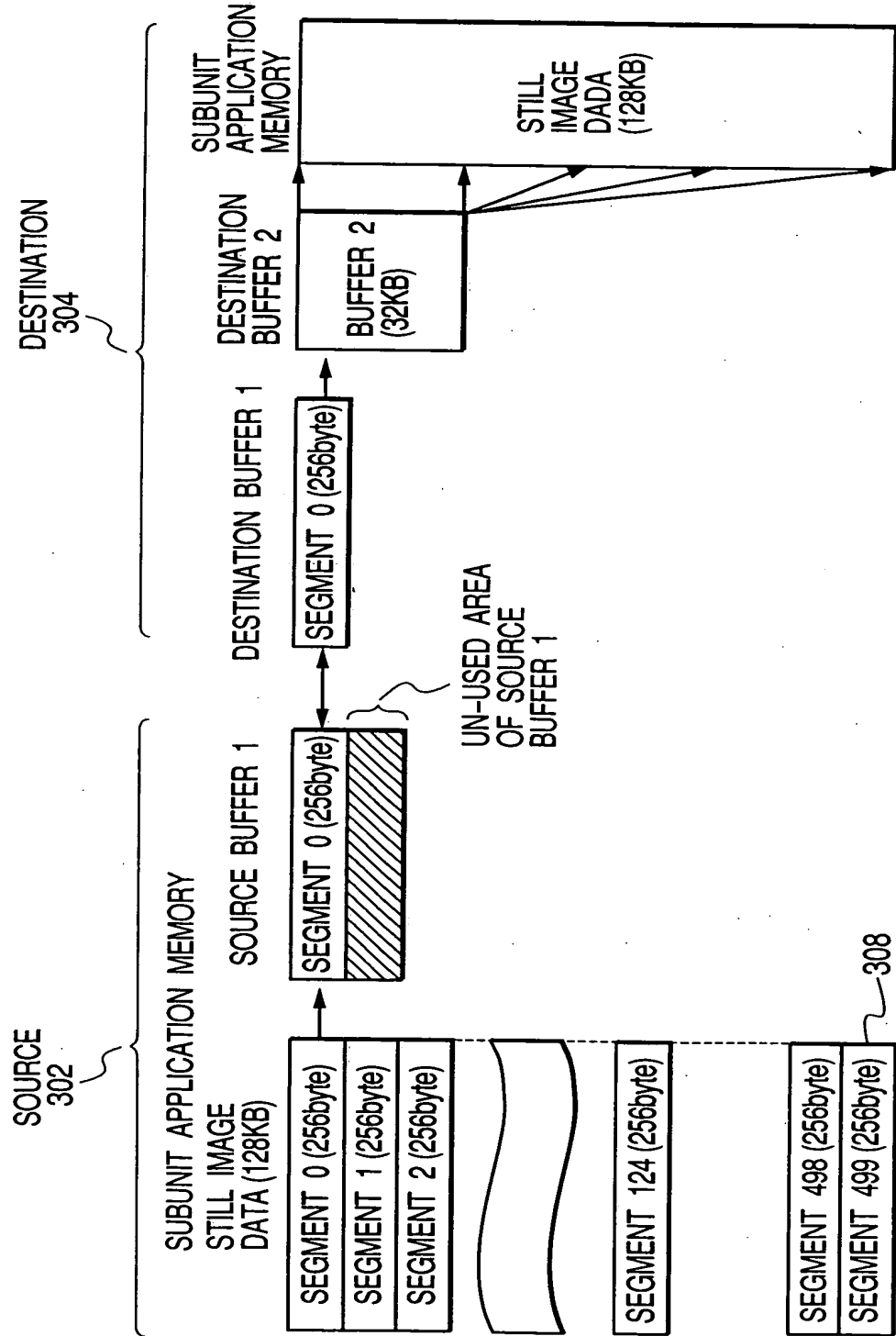


FIG. 43

BUFFER MODEL 2 SOURCE BUFFER 1 < DEST. BUFFER 1

